

COFC



P20968.A20

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s) : Y. YUDA et al.

Certificate of Corrections Branch

Patent No. : 6,922,450

Issued: July 26, 2005

Appl. No. : 09/846,390

Filed: May 2, 2001

For : DIRECTION OF ARRIVAL ESTIMATOR AND
DIRECTION OF ARRIVAL ESTIMATION METHOD

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
U.S. Patent and Trademark Office
Customer Service Window
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Certificate
NOV 08 2005
of Correction

Sir:

Please find attached a proposed Certificate of Correction.

Please correct the following errors appearing in the printed patent, which are the fault of the U.S. Patent and Trademark Office, as per the attached Certificate of Correction. Since these errors are the fault of the USPTO, no fee is due.

At column 6, line 16 (claim 11, line 4) of the printed patent, after "plurality" insert ---of---. This appeared correctly on page 4, line 5 of the Examiner's Amendment dated April 5, 2005 attached to the Office Action mailed April 15, 2005.

At column 6, lines 61-62 (claim 18, lines 3-4) of the printed patent, "performing a first calculation calculating a first cyclic" should be ---from a plurality of second modulated signals of

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the reception signal---. This appeared correctly on page 4, lines 21-22 of the Examiner's Amendment dated April 5, 2005 attached to the Office Action mailed April 15, 2005.

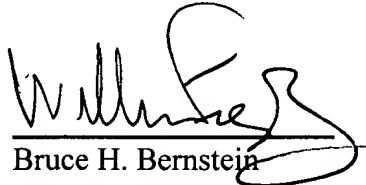
Therefore, it is respectfully requested that a Certificate of Correction issue in the above-identified patent as follows:

At column 6, line 16 (claim 11, line 4) of the printed patent, after "plurality" insert ---of---.

At column 6, lines 61-62 (claim 18, lines 3-4) of the printed patent, "performing a first calculation calculating a first cyclic" should be ---from a plurality of second modulated signals of the reception signal---.

Should there be any questions, the Examiner is invited to contact the undersigned at the below-listed number.

Respectfully submitted,
Y. YUDA et al.



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November 4, 2005
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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,922,450 B2

DATED : July 26, 2005

INVENTOR(S) : Y. YUDA et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At column 6, line 16 (claim 11, line 4) of the printed patent, after "plurality" insert ---of---.

At column 6, lines 61-62 (claim 18, lines 3-4) of the printed patent, "performing a first calculation calculating a first cyclic" should be ---from a plurality of second modulated signals of the reception signal---.

MAILING ADDRESS OF SENDER:

PATENT NO. 6,922,450

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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**PATENT NO. : 6,922,450 *B2*

DATED : July 26, 2005

INVENTOR(S) : Y. YUDA et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At column 6, line 16 (claim 11, line 4) of the printed patent, after "plurality" insert ---of---

At column 6, lines 61-62 (claim 18, lines 3-4) of the printed patent, "performing a first calculation calculating a first cyclic" should be ---from a plurality of second modulated signals of the reception signal---.

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